

## N-Channel Enhancement Mode Power MOSFET

### ● Features

$V_{DS}$	$R_{DS(ON)TYP}$	$I_D$
30V	2.7 m $\Omega$ @10V	60A
	4.3 m $\Omega$ @4.5V	

### ● General Description

HG60N30QA uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

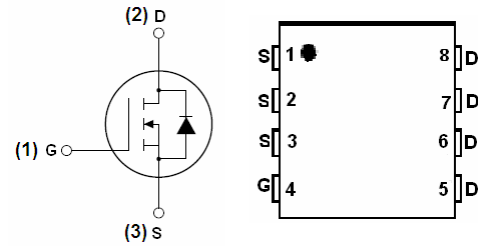
### ● Absolute Maximum Ratings @ $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current (Continuous) *AC	$I_D$	$T_A=25^\circ\text{C}$	60
		$T_A=100^\circ\text{C}$	38
Drain Current (Pulse) *B	$I_{DM}$	120	A
Single Pulse Avalanche Energy <sup>2</sup>	EAS	115	mJ
Single Pulse Avalanche Current <sup>2</sup>	IAS	48	A
Power Dissipation	$P_D$	$T_A=25^\circ\text{C}$	50
		Derate above 25 $^\circ\text{C}$	0.77
Operating Temperature/ Storage Temperature	$T_J/T_{STG}$	-55~150	$^\circ\text{C}$

### ● Thermal Characteristics

Symbol	Parameter	Typ	Max	Unit
R $\theta$ JA	Thermal Resistance Junction to ambient	--	66	$^\circ\text{C}/\text{W}$
R $\theta$ JC	Thermal Resistance Junction to Case	--	2.5	$^\circ\text{C}/\text{W}$

### ● Pin Configurations



## Electrical Characteristics

T<sub>A</sub>=25°C unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250 μ A	30	--	--	V
BV <sub>DSS</sub> Temperature Coefficient	ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Reference to 25°C, I <sub>D</sub> =1mA	--	0.04	--	V/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0V, T <sub>J</sub> =25°C	--	--	1	μ A
		V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0V, T <sub>J</sub> =125°C	--	--	10	μ A
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>DS</sub> = 250 μ A	1.2	1.7	2.5	V
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub>		--	4	--	mV/°C
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	--	--	±100	nA
Drain-Source On-state Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	--	2.7	3.5	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A	--	4.3	5.5	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 20A	22	50	--	S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>SD</sub> = 10A, V <sub>GS</sub> = 0V	--	--	1.1	V
Diode Forward Current	I <sub>S</sub>		--	--	30	A
Single Pulse Avalanche Energy	EAS	V <sub>DD</sub> = 25V, L = 0.1mH, I <sub>AS</sub> = 25A	31	115	--	mJ

### Switching

Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 15V, I <sub>D</sub> = 20A, V <sub>GS</sub> = 10V	--	45	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	5.7	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	7	--	nC
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15V, I <sub>D</sub> = 15A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = 3.3Ω	--	16	--	ns
Turn-on Rise Time	t <sub>r</sub>		--	10	--	ns
Turn-off Delay Time	t <sub>d(off)</sub>		--	46	--	ns
Turn-Off Fall Time	t <sub>f</sub>		--	18	--	ns

### Dynamic

Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz	--	2340	--	pF
Output Capacitance	C <sub>oss</sub>		--	580	--	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	220	--	pF
Gate resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, F = 1MHz	--	1.5	--	Ω

### ● Drain-Source Diode Characteristics

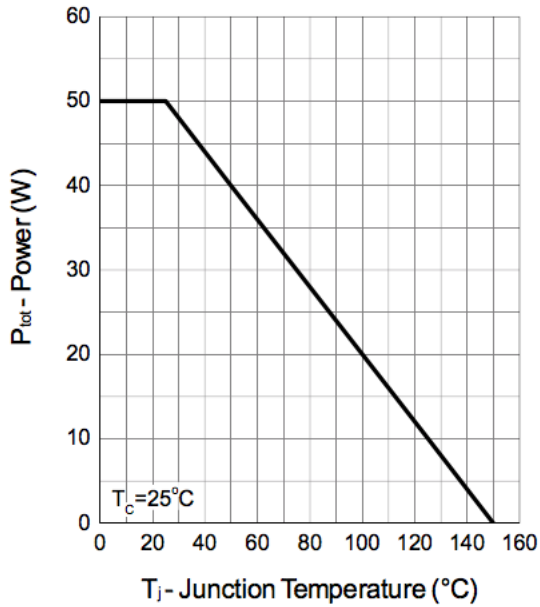
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Continuous Source Current	I <sub>S</sub>	V <sub>G</sub> = V <sub>D</sub> = 0V, Force Current	--	30	--	A
Pulsed Source Current <sup>3</sup>	I <sub>SM</sub>		--	--	120	A
Diode Forward Voltage <sup>3</sup>	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A, T <sub>J</sub> = 25°C	--	--	1.1	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> = 30V, I <sub>S</sub> = 1A, di/dt = 100A/μs T <sub>J</sub> = 25°C	--	27.5	--	ns
Reverse Recovery Charge	Q <sub>rr</sub>		--	17	--	nC

Note :

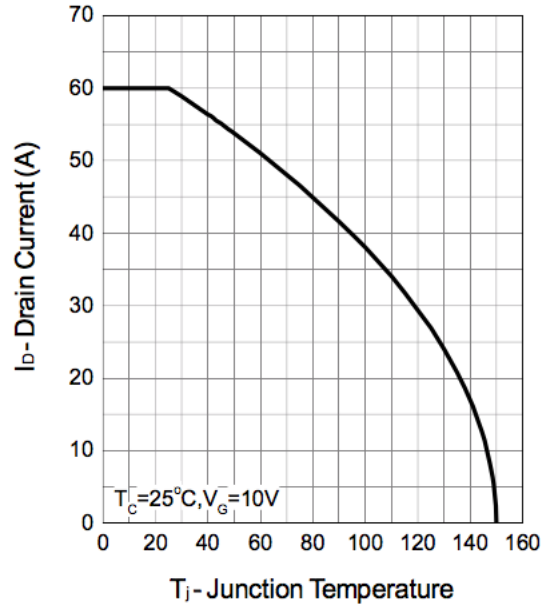
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub> = 25V, V<sub>GS</sub> = 10V, L = 0.1mH, I<sub>AS</sub> = 50A., R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C.
3. The data tested by pulsed, pulse width ≅ 300us, duty cycle ≅ 2%.
4. Essentially independent of operating temperature.

# Typical Electrical and Thermal Characteristics

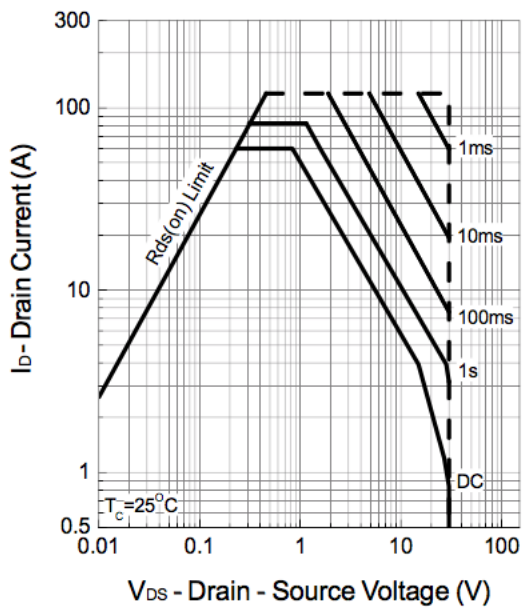
**Power Dissipation**



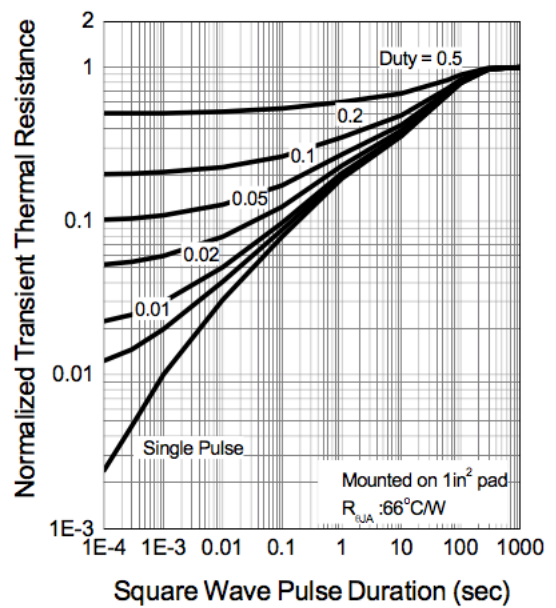
**Drain Current**



**Safe Operation Area**

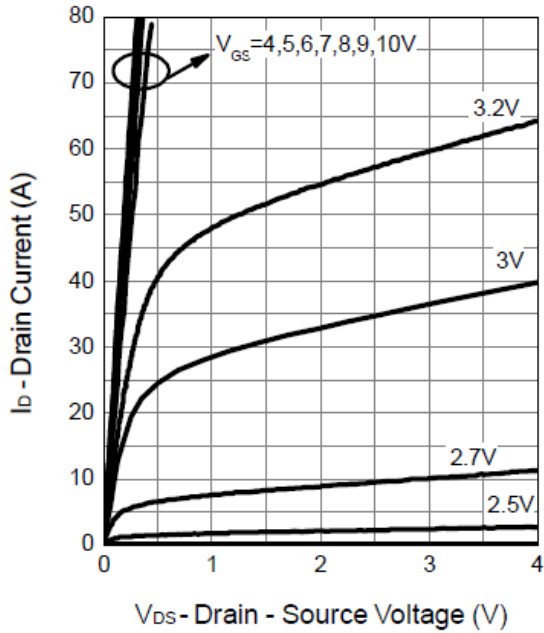


**Thermal Transient Impedance**

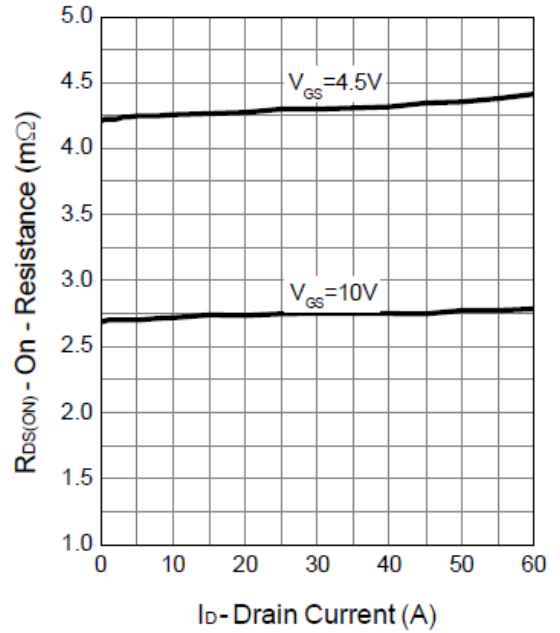


# Typical Electrical and Thermal Characteristics

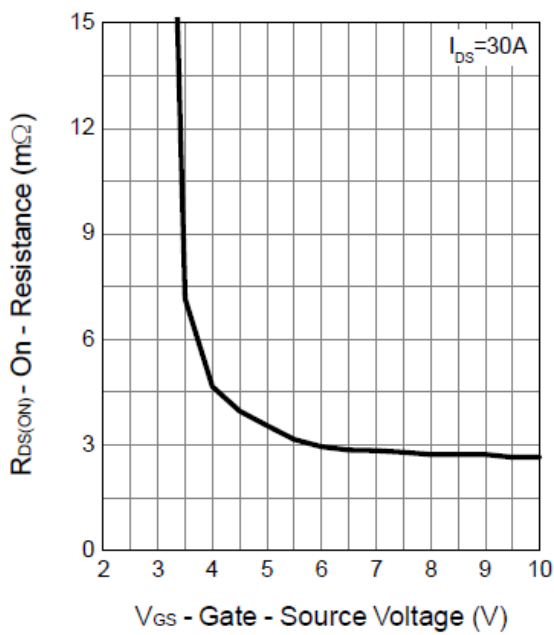
**Output Characteristics**



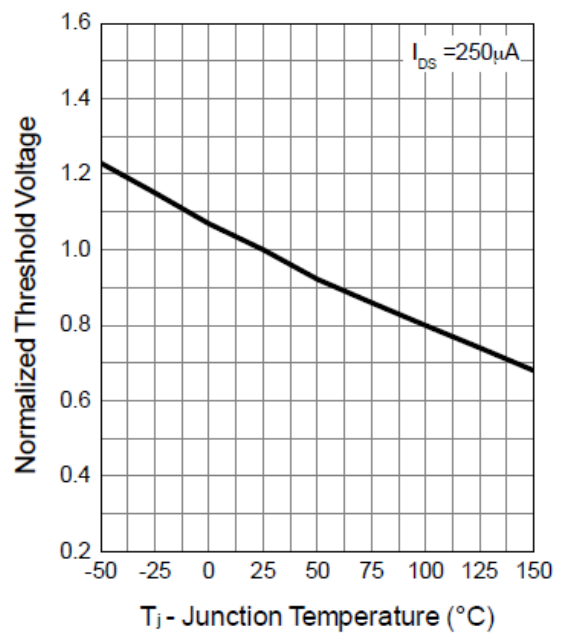
**Drain-Source On Resistance**



**Gate-Source On Resistance**

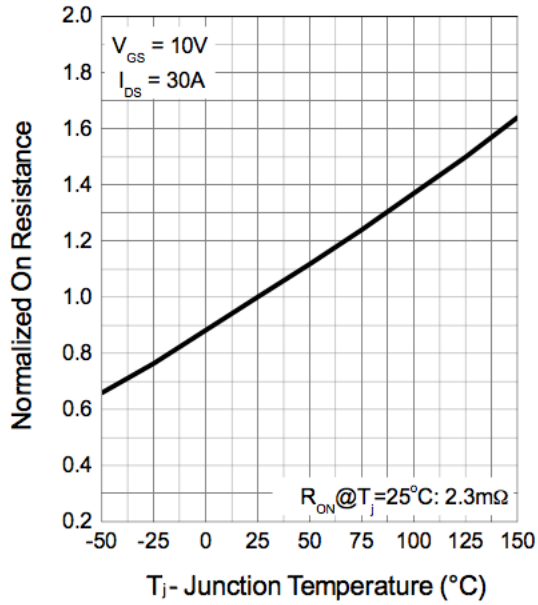


**Gate Threshold Voltage**

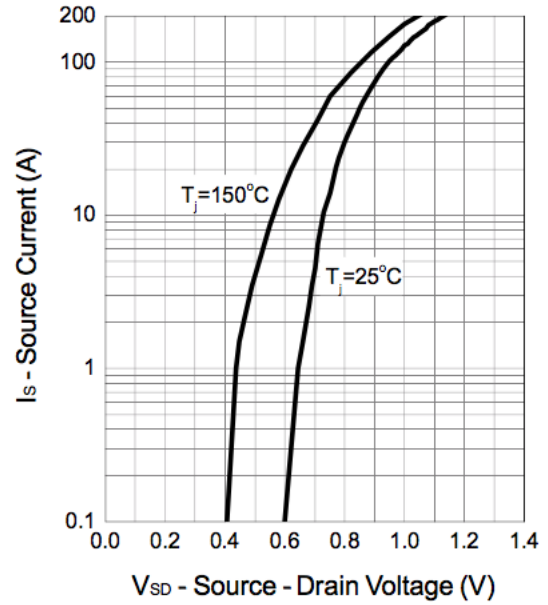


# Typical Electrical and Thermal Characteristics

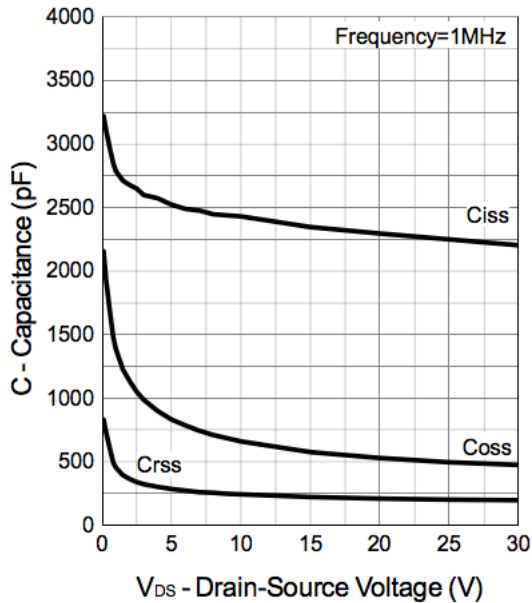
**Drain-Source On Resistance**



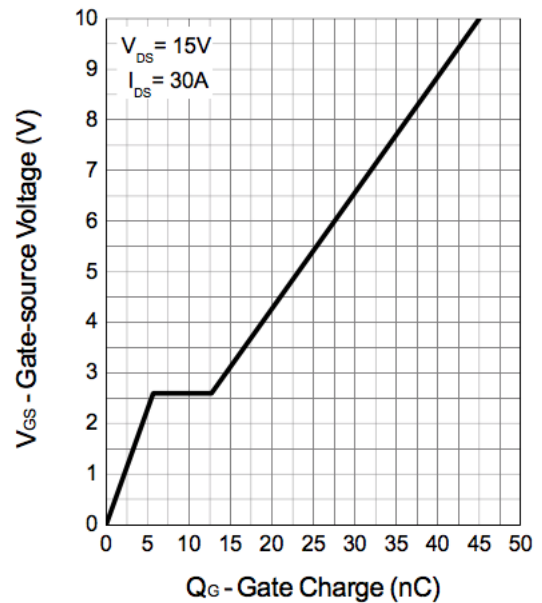
**Source-Drain Diode Forward**



**Capacitance**

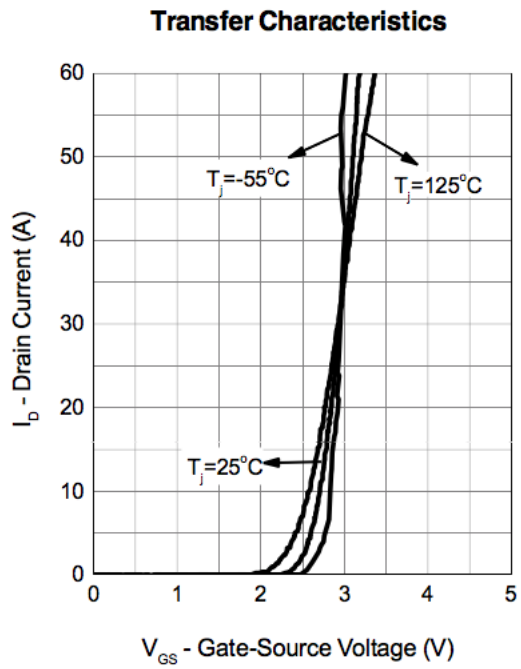


**Gate Charge**



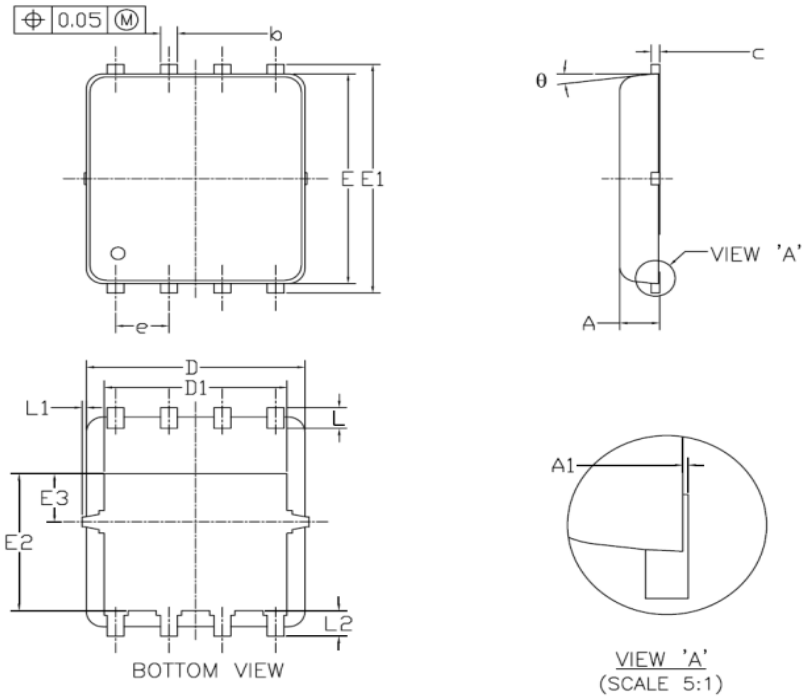
# Typical Electrical and Thermal Characteristics

---

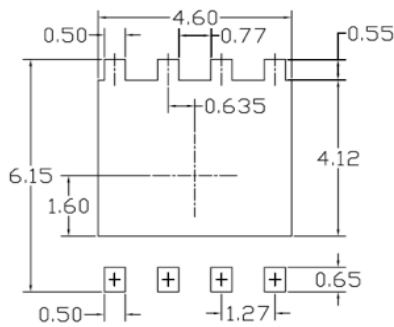


# Package Information

DFN5x6\_8L\_EP1\_P PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	0.95	1.00	0.033	0.037	0.039
A1	0.00	—	0.05	0.000	—	0.002
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.15	0.20	0.25	0.006	0.008	0.010
D	5.20 BSC			0.205 BSC		
D1	4.35 BSC			0.171 BSC		
E	5.55 BSC			0.219 BSC		
E1	6.05 BSC			0.238 BSC		
E2	3.625 BSC			0.143 BSC		
E3	1.275 BSC			0.050 BSC		
e	1.27 BSC			0.050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026
L1	0	—	0.15	0	—	0.006
L2	0.68 REF			0.027 REF		
θ	0°	—	10°	0°	—	10°

**NOTE**

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.  
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
2. CONTROLLING DIMENSION IS MILLIMETER.  
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

UNIT: mm